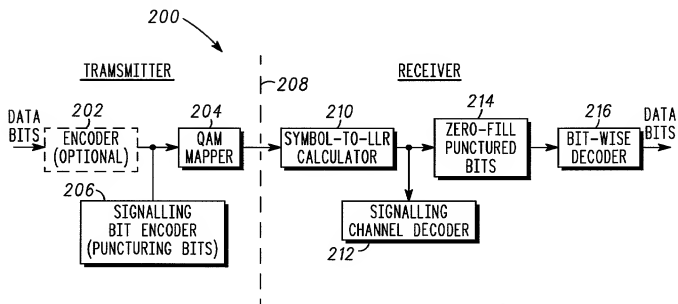


FIG. 2



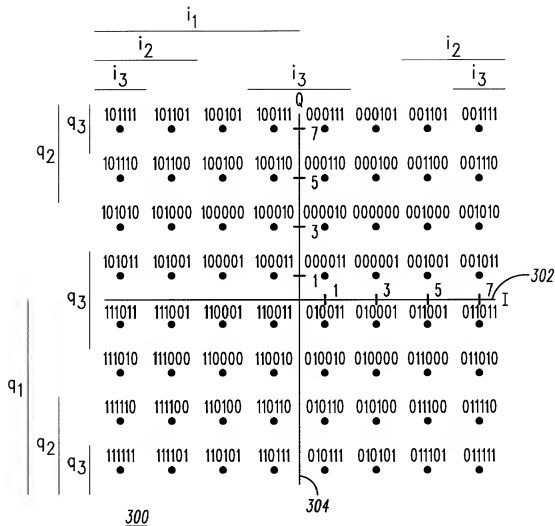


FIG. 3

BIT POSITION	DEPENDENCY ON OTHER BITS	LLR
BIT b_0	NONE	$\pm 4D^2$
BIT b_1	NONE	$\pm 4D^2$
BIT b_2	BIT $b_0=1$	$\pm 16D^2$
	BIT $b_0=0$	$\pm 4D^2$
BIT b_3	BIT $b_1=1$	$\pm 16D^2$
	BIT $b_1=0$	$\pm 4D^2$
BIT b_4	BIT $b_2=1$, BIT $b_0=1$	$\pm 64D^2$
	BIT $b_2=1$, BIT $b_0=0$	$\pm 36D^2$
	BIT $b_2=0$, BIT $b_0=0$	$\pm 16D^2$
	BIT $b_2=0$, BIT $b_0=1$	$\pm 4D^2$
BIT b_5	BIT $b_3=1$, BIT $b_1=1$	$\pm 64D^2$
	BIT $b_3=1$, BIT $b_1=0$	$\pm 36D^2$
	BIT $b_3=0$, BIT $b_1=0$	$\pm 16D^2$
	BIT $b_3=0$, BIT $b_1=1$	$\pm 4D^2$

FIG. 4

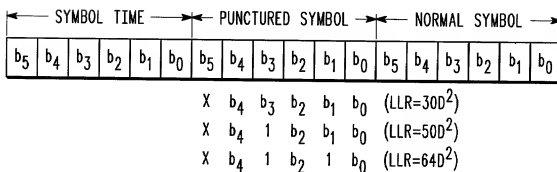


FIG. 5